JOB OPENING

Hardware and FPGA Designer

Primary responsibility is to develop FPGA based and board-level hardware for next-generation high-complexity, multi-core processors for security, surveillance and reconnaissance applications. This includes original FPGA design work together with the reuse of existing FPGA-based SIMD, MIMD and pipeline processor designs from previous, successful projects.
The successful candidate will have strong, proven experience in signal processing, image flow processing and related algorithmic development and optimization. The successful candidate will be able to develop, analyze and verify VHDL- and Verilog-based designs and be familiar with the various stages of FPGA design flows, including architecture determination, logic design, implementation/synthesis and, functional and timing verification. The successful candidate will also have board-level hardware design experience with FPGAs.

Specific responsibilities include:

- Develop specification, architecture, and micro-architecture for a well-defined class of signal processing and communications algorithms
- Bit-exact MATLAB and/or C/C++ system modeling and simulation
- Development/simulation of RTL hardware implementations in VHDL and Verilog
- FPGA based prototyping
- Hardware schematic design and layout optimization
- Documentation/application note development and customer support

Experience:
Candidate must have MS-EE degree or equivalent and at least 3 years of relevant experience. FPGA design experience is a necessity.

Skills required:

- Candidate must have hands-on experience in VHDL and/or Verilog design and verification
- Candidate should have the skills to successfully complete the full development cycle for an FPGA-based processor, and ideally, should have done this previously.
- Candidate must have hands-on experience in the development of FPGA designs for XILINX and/or ALTERA platforms.
- Candidate must have experience in board-level design incorporating FPGAs (schematics capture and layout optimization)
- Demonstrable good documentation skills regarding code commenting and engineering specifications

Knowledge of tools:
Xilinx ISE-EDK, Altera Quartus, Mentor Graphics ModelSim, Aldec Active-HDL, Mathworks MATLAB, GCC (on various embedded platforms), MentorGraphics PADS and Altium Designer.

Work permission status:
The successful candidate must be presently eligible to work permanently in the US.

Applications with CVs should be sent to: *job@eutecus.com*
(refer to position number HWFPGA)

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